

Applicant : George P. Merrill et al.
Serial No. : 10/060,865
Filed : January 29, 2002
Page : 4

Attorney's Docket No.: 10559-738001 / P13587

Remarks

Applicant submits herewith new claims 31-40. Additionally, the applicant has modified the specification to correct a typographical error. Further, applicant has modified figure 2 to correct a text block label within that figure.


Attached is a marked-up version of the changes being made by the current amendment to figure 2.

Applicant asks that all claims be examined. Enclosed is a \$264.00 check for excess claim fees. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date:

April 27, 2002


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Version with markings to show changes made

In the specification:

Paragraph beginning at page 2, line 18 has been amended as follows:

Typically, slave processors 14, 16 and 18 are programmable state machines that control the flow of data retrieved from a data port or bus. For example, slave [process] processor 14 may control the transmission and retrieval of data on a WAN (wide-area network) and slave processors 16 and 18 may control the transmission and retrieval of data on LANs (local-area networks), such as 10BaseT and 100BaseTx Ethernet networks.

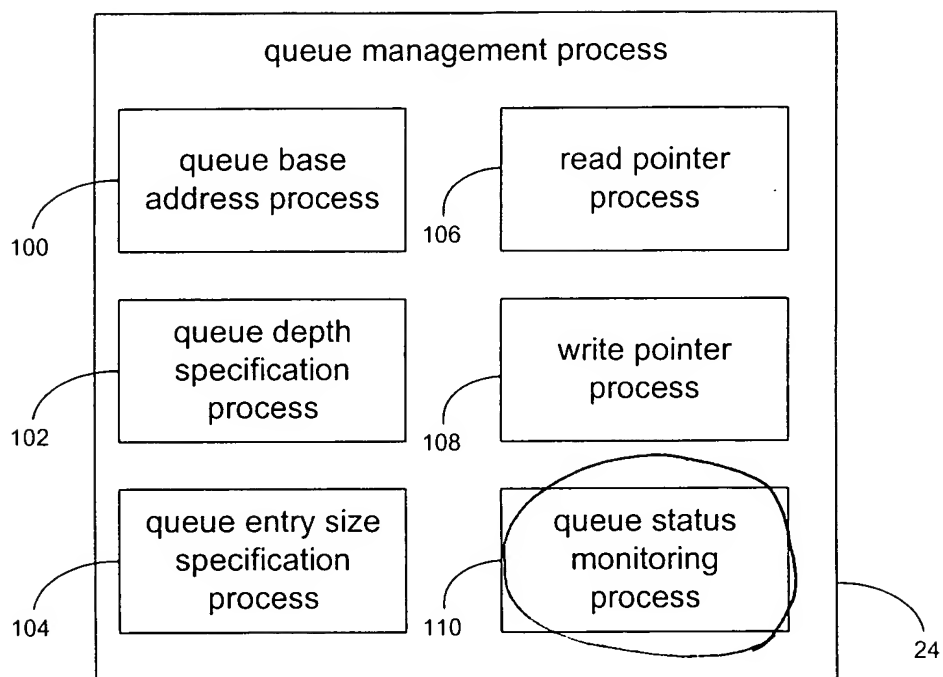


Fig. 2